

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Damian Aquino on 01/09/08.

2. The application has been amended as follows:

IN THE DRAWINGS:

Amend Figure 4 according to the attached document (replacement figure 4) in the following:

In Figure 4, arrow from transmitter 72 point to transmitter 76 has been replaced by arrow from transmitter 72 point to receiver 78.

In Figure 4, arrow from receiver 78 point to receiver 74 has been replaced by arrow from transmitter 76 point to receiver 74.

In Figure 4, arrow from transmitter 80 point to transmitter 84 has been replaced by arrow from transmitter 80 to receiver 86.

In Figure 4, arrow from receiver point to receiver 82 has been replaced by arrow from transmitter 84 point to receiver 82.

IN THE SPECIFICATION:

In paragraph [0027], line 4, the phrase “includes a transmitter and a receiver.” has been replaced by --- includes a transmitter (e.g. transmitters 72, 76, 80, and 84) and a receiver (e.g. receivers 74, 78, 82, and 86). ---.

IN THE CLAIMS:

Claims 94-97, 104-111, 153-156, and 163-170 have been cancelled.

Allowable Subject Matter

3. The following is an examiner’s statement of reasons for allowance:

Regarding claims 13 and 85, the prior art fails to teach a combination of the steps of:

wherein after said receiver of said second GBIC interface receives a second configuration ordered set from said transmitter of said first GBIC interface and said GBIC module stores in memory first configuration data of said first device that is contained in said second configuration ordered set, said transmitter of said first copper interface transmits a first fast link pulse (FLP) burst, in the specific combination as recited in the claims.

Regarding claim 28, the prior art fails to teach a combination of the steps of:

wherein said transmitter of said first copper interface does not transmit a fast link pulse (FLP) burst until said transmitter of said first GBIC interface transmits said first configuration ordered set, in the specific combination as recited in the claim.

Regarding claims 46 and 124, the prior art fails to teach a combination of the steps of:

wherein after said receiver of said second network interface receives a second configuration ordered set from said transmitter of said first network interface and said network interfacing means stores in memory first configuration data of said first means that is contained in said second configuration ordered set, said transmitter of said first copper interface transmits a first fast link pulse (FLP), in the specific combination as recited in the claims.

Regarding claims 65 and 143, the prior art fails to teach a combination of the steps of:

transmitting a first fast link pulse (FLP) burst using said transmitter of said first copper interface after said receiver of said second GBIC interface receives a second configuration ordered set from said transmitter of said first GBIC interface and said GBIC module stores in memory first configuration data of said first device that is contained in said second configuration ordered set, in the specific combination as recited in the claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to TOAN D. NGUYEN whose telephone number is (571)272-3153. The examiner can normally be reached on M-F (7:00AM-4:30PM).

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, William Trost can be reached on 571-272-7872. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/T. D. N./
Examiner, Art Unit 2416

/William Trost/
Supervisory Patent Examiner, Art Unit 2416